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О РМ РТО-1082

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

9 8 U

Ae application of:

unichi KARASAWA and Kunio WATANABE, both of Nagano-ken, Japan

erial No: Not Assigned

Filed: February 6, 2002

For: Semiconductor Device, Memory System and Electronic Apparatus

Box PATENT APPLICATION

Box PATENT APPLICATION Commissioner for Patents Washington, D.C. 20231

Dear Sir:

CORRESPONDENCE ADDRESS:

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ITEM		TOTAL NO. OF CLAIMS		NO. OF CLAIMS OVER BASE	LG/SM \$ ENTITY FEE		\$ AMOUNT	\$ FEE
Α.	TOTAL CLAIMS FE E	39	-20	19	LG=\$18 SM=\$9	\$18	342 00	
В	INDEPENDENT CLAIMS FEE*	1	-3	0	LG=\$84 SM=\$42	\$84	\$ 0	
В	C SUBTOTAL - ADDITIONAL CLAIMS FEE (ADD FINAL COLUMN IN LINES A + B) LARGE ENTITY FEE = \$280 MULTIPLE-DEPENDENT CLAIMS FEE SMALL ENTITY FEE = \$140							342 00
D								\$ 280.00
E BASIC FEE SATURE SMALL ENTITY FEE = \$740 F TOTAL FILING FEE (ADD TOTALS FOR LINES C, D, AND E)								\$ 740.00
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G ASSIGNMENT RECORDING FEE \$ 40 *LIST INDEPENDENT CLAIMS 1						\$ 40		

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Date: February 6, 2002

500 South Grand Avenue, Suite 1900

Los Angeles, CA 90071

Telephone: Facsimile:

(213) 337-6700

(213) 337-6701

Respectfully submitted, HOGAN & HARTHON

Anthony J. Orler

Registration No. 41,232 Attorney for Applicant(s)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

	In re application of:	Art Unit:	Not Assigned		
	Junichi KARASAWA and Kunio WATANABE	Examiner:	Not Assigned		
	Serial No: Not Assigned				
	Filed: February 6, 2002				
	For: Semiconductor Device, Memory System and Electron Apparatus	nic			
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